



DOCKET NO.: 240416US0DIV

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

ATTORNEYS AT LAW

Re: Patent No.: 6,936,484
Serial No.: 10/618,085
Issue Date: August 30, 2005
Patentee(s): Masakazu KANECHIKA, et al.
For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE

SIR:

Attached hereto for filing are the following papers:

**REQUEST FOR CERTIFICATE OF CORRECTION
CERTIFICATE OF CORRECTION FORM PTO 1050 (IN DUPLICATE)**

Our check in the amount of \$ 0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. §1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. §1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Kirsten Grueneberg
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**Certificate
NOV 18 2005
of Correction**



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Docket No: 240416US0DIV



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE PATENT OF: Masakazu KANECHIKA, et al.

PATENT NUMBER: 6,936,484

ISSUED: August 30, 2005

FOR: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE

REQUEST FOR CERTIFICATE OF CORRECTION

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

The following is a request for a certificate of correction in Serial Number 10/618,085, now Patent Number 6,936,484.

In accordance with 37 C.F.R. § 1.322, which implements 35 USC §254, the Patent Office is respectfully requested to issue a certificate of correction in the above-identified patent.

All of the errors are the fault of the Patent Office. Accordingly, no fee is required. The requested corrections are shown on the attached Form PTO-1050.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,936,484

DATED: August 30, 2005

INVENTOR(S): Masakazu KANECHIKA, et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 46

Claim 16 should read:

--A method as defined in claim 15, wherein the semiconductor material substrate or layer comprises silicon; the first impurity is oxygen; and the second impurity is boron.--

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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DATED: August 30, 2005

INVENTOR(S): Masakazu KANECHIKA, et al.

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Column 46

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